IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

KUNII et al.

Group Art Unit: Unassigned

Application No. Unassigned

Examiner: Unassigned

Filed: December 1, 2003

For: SEMICONDUCTOR DEVICE

INFORMATION DISCLOSURE STATEMENT

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Pursuant to 37 CFR 1.97 and 1.98, the references listed on the enclosed Form PTO-1449 and/or Substitute Form PTO-1449 ("Form 1449") are submitted for consideration by the Examiner in the examination of the above-identified patent application.

The full consideration of the references in their entirety by the Examiner is respectfully requested and encouraged. Also, it is respectfully requested that the references be entered into the record of the present application and that the Examiner place his or her initials in the appropriate area on the enclosed Form 1449, thereby indicating the Examiner's consideration of each of the references.

The submission of the references listed on the Form 1449 is for the purpose of providing a complete record and is not a concession that the references listed thereon are prior art to the invention claimed in the patent application. The right is expressly reserved to establish an invention date earlier than the above-identified filing date in order to remove any reference submitted herewith as prior art should it be deemed appropriate to do so.

Further, the submission of the references is not to be taken as a concession that any reference represents art that is relevant or analogous to the claimed invention. Accordingly, the right to argue that any reference is not properly within the scope of prior art relevant to an examination of the claims in the above-identified application is also expressly reserved.

The Information Disclosure Statement is being filed:

within any one of the following time periods: (a) within three months of the filing date of a national application other than a continued prosecution application under 37 CFR 1.53(d); (b) within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 of an international application; (c) before the mailing date

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	of a fir	rst Office Action on the merits; or (d) before the mailing of a first Office Action ne filing of a request for continued examination under 37 CFR 1.114.
	37 CF	(a), (b), (c) or (d) above, but before the mailing date of a final action under R 1.113, a Notice of Allowance under 37 CFR 1.311, or an action that vise closes prosecution in the application, and includes <i>one</i> of:
		the Statement under 37 CFR 1.97(e) (see "Statement under 37 CFR 1.97(e)" below).
	\Box	the fee of \$180 set forth in 37 CFR 1.17(p) (see "Fees" below).
	under and o 37 CF	the mailing date of a final action under 37 CFR 1.113 or a Notice of Allowance 37 CFR 1.311, or an action that otherwise closes prosecution in the application, n or before payment of the issue fee, and includes the Statement under R 1.97(e) (see "Statement under 37 CFR 1.97(e)" below), and the fee of \$180 as th in 37 CFR 1.17(p) (see "Fees" below).
	payme contai 37 CF \$180 a NOTE: May 29	the mailing date of a Notice of Allowance under 37 CFR 1.311, and on or before ent of the issue fee, and within thirty days of receiving each item of information ned in the Information Disclosure Statement, and includes the Statement under R 1.704(d) (see "Statement under 37 CFR 1.704(d)" below), and the fee of as set forth in 37 CFR 1.17(p) (see "Fees" below). This is for original applications except applications for a design patent, filed on or after 2000, wherein a paper containing only an Information Disclosure Statement in compliance CFR 1.97 and 1.98 is being filed.
Copie	s of the	References
	Attach releva an En action degree	s of the references listed on the enclosed Form 1449 are enclosed herewith, need to each reference not in the English language is a concise explanation of the nee pursuant to 37 CFR 1.98(a)(3). An English-language equivalent/patent, or glish-language abstract, or an English-language version of the search report or by a foreign patent office in a counterpart foreign application indicating the of relevance found by the foreign office is being submitted in lieu of a concise nation of the relevance pursuant to 37 CFR 1.98(a)(3).
	A cop	y of the foreign search report is enclosed herewith.
	parent furnish submi The laccord Proced relied	eferences listed on the enclosed Form 1449 were previously identified in the application(s) of the present application, and copies of the references were need at that time. Accordingly, additional copies of the references are not ted herewith, so as not to burden the file with duplicate copies of references. Examiner is respectfully requested to carefully review the references in lance with the requirements set out in the Manual of Patent Examining dure. In accordance with 37 CFR 1.98(d), the details of the parent application(s) upon for an earlier filing date under 35 USC 120 in which copies of the nees were previously furnished are set out below:

U.S. APPLI	Status (check one)			
U.S. APPLICATIONS	U.S. FILING DATE	PATENTED	PENDING	ABANDONED
1.				
2.				
3.				

3.					
Staten	nent under 37 CFR	1.97(e)			
	Information Discloring foreign patent office	hereby states that each sure Statement was fi te in a counterpart forei filing of the Information	rst cited in a gn patent appl	ny commun ication not r	ication from a
	Information Disclosion office in a counte undersigned after n Information Disclosion	hereby states that no sure Statement was cited rpart foreign patent appaking reasonable inquires sure Statement was been than three months out.	in a communi plication, and y, no item of item of item of item of item of item of any	cation from a to the known to t	a foreign patent owledge of the contained in the designated in
Staten	nent under 37 CFR	1.704(d)			
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Fees					
	No fee is owed by the IDS Fee of \$18	he applicant(s). 30 under 37 CFR 1.17(p)	is enclosed he	erewith.	
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for that purpose.)

In re Appln. of KUNII et al. Application No. Unassigned

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Instructions as to Overpayment

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Respectfully submitted,

effrey A. Wyand, Reg. No. 29,458 EYDIG, VOIT & MAYER

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IDS (Revised 8/26/03)

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Su	ıbstitute for f	orm 1449A/B/PTO			Application Number	Unassigned	
	INIE	ODMATION	DISC	LOCUDE	Filing Date	December 1, 2003	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				First Named Inventor	Tetsuo KUNII		
	STA	LEMENIB	Y API	PLICANI	Group Art Unit	Unassigned	
		(Use as many shee	ets as nece	essary)	Examiner Name	Unassigned	
	Sheet	1 1	of	1	Attorney Docket Number	402887/AOYAMA	

		OTHER - NON PATENT LITERATURE DOCUMENTS		slation
Examiner Initials	Doc. No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number (s), publisher, city and/or country where published.		
	A 1	LOKE et al., "Copper Drift in Low-K Polymer Dielectrics for ULSI Metallization," 1998 Symposium on VLSI Tech., June 9-11, 1998, Honolulu, HI.		
	A 2	WEB PAGE: Ireland Integrated Circuit Technology Research Institute NMRC, pp. 1-2, 10 October 2003.		
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Examiner Date Considered Signature

^{*} A concise statement of relevance is being submitted in lieu of a translation. 37 CFR 1.98(a)(3).

⁺ An English-language equivalent/patent, or an English-language abstract, or an English-language version of the search report or action by a foreign patent office in a counterpart foreign application indicating the degree of relevance found by the foreign office is being submitted in lieu of a concise explanation of relevance under 37 CFR 1.98(a)(3).

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Substitute for form 1449A/B/PTO

Examiner

Signature

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)
Sheet 1 of

	Complete if Known	
Application Number	Unassigned	
Filing Date	December 1, 2003	
First Named Inventor	Tetsuo KUNII	
Group Art Unit	Unassigned	
Examiner Name	Unassigned	
Attorney Docket Number	402887/AOYAMA	

		OTHER - NON PATENT LITERATURE DOCUMENTS	7	-1-4
Examiner Initials	Doc. No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number (s), publisher, city and/or country where published.	Yes	slation No*+
	A 1	LOKE et al., "Copper Drift in Low-K Polymer Dielectrics for ULSI Metallization," 1998 Symposium on VLSI Tech., June 9-11, 1998, Honolulu, HI.		
	A 2	WEB PAGE: Ireland Integrated Circuit Technology Research Institute NMRC, pp. 1-2, 10 October 2003.		
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Date

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